

DEC 13 2007**TROP, PRUNER & HU, P.C.**

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Company:	US Patent Office	Date:	December 13, 2007
Fax:	(571) 273-8300	Pages:	7 (including cover sheet)
Your Re:	10/750,075	Our Re:	ITL.1703US

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	Louis Lippincott et al.	§	Art Unit:	2621
Serial No.:	10/750,075	§	Examiner:	Chikaodili E. Anyikire
Filed:	December 31, 2003	§	Docket:	ITL.1703US P17498
For:	Motion Estimation Sum of all Differences (SAD) Array Having Reduced Semiconductor Die Area Consumption	§	Assignee:	Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed November 7, 2007, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made to the claims.

Date of Deposit: December 13, 2007
I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8300) on the date indicated above.
Cynthia L. Hayden
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